

The present disclosure relates to a disk drive (10) comprising a rotating disk (12) with a recording surface (14), a transducer (16) in communication with the surface and swept radially over the surface by a servo-actuator (18), a variable gain read amplifier (20) connected to the transducer, an analog-to-digital converter ADC (22) attached to the variable gain amplifier, an erase frequency oscillator (24) coupled to the transducer for erasing of the disk surface, a memory (26) for storing digital outputs appearing at the ADC, and a controller for signalling the servo-actuator (30) to move to such radial positions that result in transducer read amplitudes that are a percentage of previous read amplitudes represented in the digital memory. Bit-synchronism between tracks is maintained by writing an initial clock track with closure and then writing a next clock track including a regular sequence of clock bursts a half-track space offset such that the initial clock track can be read in between writing clock bursts and the read signal is used to frequency-lock an oscillator (29) which is used as a reference for the writing of clock bursts of the next track. A checkerboard pattern of clock bursts is thus created.

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SELF-SERVOWRITING DISK DRIVE AND METHOD

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BACKGROUND OF THE INVENTION1. Field of the Invention

The invention relates generally to hard disk drive memory storage devices for computers and more specifically to disk drive apparatus and method for writing servotrack information and alleviating the need for a separate servowriter to establish such servopatterns on the recording surfaces of the recording media.

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2. Description of the Prior Art

Increased levels of storage capacity in floppy and hard disk drives are a direct result of the higher track densities possible with voice-coil and other types of servo positioners. Previously, low track density disk drives were able to achieve satisfactory head positioning with leadscrew and stepper motor mechanisms. However, when track densities become so great that the mechanical error of a leadscrew-stepper motor combination is significant compared to track-to-track spacing, an embedded servo is needed so that the position of the head can be determined from the signals it reads.

Conventional hard disk manufacturing techniques include writing servotracks on the media of a head disk assembly (HDA) with a specialized servowriter instrument. Laser positioning feedback is used in such instruments to read the actual physical position of a recording head used to write the servotracks. Unfortunately, it is becoming more and more difficult for such servowriters to invade the internal environment of a HDA for servowriting because the HDAs themselves are exceedingly small and depend on their covers and castings to be in place for proper operation. Some HDAs are the size and thickness

of a plastic credit card. At such levels of microminiaturization, traditional servowriting methods are inadequate.

Conventional servo-patterns typically comprise short bursts of a constant frequency signal, very precisely located offset from a data track's center line, on either side. The bursts are written in a sector header area, and can be used to find the center line of a track. Staying on center is required during both reading and writing. Since there can be between seventeen to sixty, or even more, sectors per track, that same number of servo data areas must be dispersed around a data track. These servo-data areas allow a head to follow a track center line around a disk, even when the track is out of round, as can occur with spindle wobble, disk slip and/or thermal expansion. As technology advances provide smaller disk drives, and increased track densities, the placement of servo data must also be proportionately more accurate.

Servo-data are conventionally written by dedicated, external servowriting equipment, and typically involve the use of large granite blocks to support the disk drive and quiet outside vibration effects. An auxiliary clock head is inserted onto the surface of the recording disk and is used to write a reference timing pattern. An external head/arm positioner with a very accurate lead screw and a laser displacement measurement device for positional feedback is used to precisely determine transducer location and is the basis for track placement and track-to-track spacing. The servo writer requires a clean room environment, as the disks and heads will be exposed to the environment to allow the access of the external head and actuator.

A method for writing a servo-pattern with a disk drive's own pair of transducers is described by Janz in United States Patent 4,912,576, issued March 27, 1990. Three types of servo-patterns are used to generate three-

phase signals that provide a difference signal having a slope that is directly proportional to velocity. Servo-patterns that are substantially wider radially than the nominal track-to-track separation are possible. This helps improve readback amplitudes, and thus servo performance. Janz observes that the signal level from a transducer is a measure of its alignment with a particular pattern recorded on the disk. If the flux gap sweeps only forty percent of a pattern, then the read voltage will be forty percent of the voltage maximum obtainable when the transducer is aligned dead-center with the pattern. Janz uses this phenomenon to straddle two of three offset and staggered patterns along a centerline path intended for data tracks.

15 In a preferred process, Janz reserves one side of a disk for servo and the other side for data. The disk drive includes two transducers on opposite surfaces that share a common actuator. To format an erased disk for data initialization, a first phase servo is written on the servo side at an outer edge. The transducers are then moved-in radially one half of a track, as indicated by the first phase servotrack amplitude, and a first data-track is recorded on the data side. The transducers are again moved-in radially one half of a track, this time as indicated by the first data-track amplitude, and a second phase servotrack is recorded on the servo side. The transducers are again moved-in radially one half of a track, as indicated by the second phase servotrack amplitude, and a second data-track is recorded on the data side. The transducers are moved-in radially another one half of a track, as indicated by the second data-track amplitude, and a third phase servotrack is recorded on the servo side. The transducers are moved-in radially one half of a track, as indicated by the third phase servotrack amplitude, and a third data-track is recorded on the data side. This back-and-forth progress is repeated until the entire two surfaces are written. If

too few or too many tracks were thus written, the disk is reformatted once more, but with a slight adjustment to step inward slightly more or slightly less than one-half a track width, as appropriate. Once the disk drive has been formatted with an entire compliment of properly spaced servotracks, the data-tracks have served their purpose and are erased in preparation of receiving user data. The circuitry to implement the method is described as not being a permanent part of the disk drive mechanism.

Unfortunately, the method described by Janz consumes one entire disk surface for servotracks and requires two heads working in tandem. Track-to-track bit synchronism is also not controlled, and seek times to find data between tracks would thus be seriously and adversely impacted. Transducer flying height variations and spindle runout that occur within a single revolution of the disk and media inconsistencies can and do corrupt radial position determinations that rely on a simple reading of off-track read signal amplitudes. Prior art methods are inadequate for very high performance disk drives.

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SUMMARY OF THE PRESENT INVENTION

It is therefore an object of the present invention to provide a method to enable servo patterns to be written very accurately on data surfaces inside a sealed drive head disk assembly (HDA).

Another object of the present invention is to provide a method to allow sector header data, including but not limited to a Grey code or other code for identifying the track number, to be written on every data surface of a disk drive.

Yet another object of the present invention is to

provide a method to allow a user to change a track pitch, a number of sectors per track, a Grey code, a drive speed or a write frequency, by modifying parameters within instructions for an embedded microprocessor.

5 A further object of the present invention is to provide a method for allowing a servo pattern to be written that is wider and/or narrower than a recording transducer.

10 Another object of the present invention is to provide a disk drive capable of writing its own servotracks with circuitry that is a permanent part of the disk drive.

15 A further object of the present invention is to provide a disk drive capable of writing its own servotracks using only one transducer and one disk surface.

20 Another object of the present invention is to provide a disk drive capable of writing its own servotracks and that maintains bit-synchronism between tracks wherein Grey Code sector header data may be included.

25 Briefly, a preferred embodiment of the present invention includes a hard disk drive comprising a rotating disk with a recording surface, a transducer in communication with the surface and servo-actuator means for radially sweeping the transducer over the surface, a variable gain read amplifier connected to the transducer, an analog to digital converter (ADC) attached to the variable gain amplifier, an erase frequency oscillator
30 coupled to the transducer for DC erasing of the disk surface, a memory for storing digital outputs appearing at the ADC, and a controller for signaling the servo-actuator to move to such radial positions that result in transducer read amplitudes that are a percentage of
35 previous read amplitudes represent in the digital memory. Bit-synchronism between tracks is maintained by writing an initial clock track with closure and then writing a

next clock track including a regular sequence of clock bursts a half-track space offset such that the initial clock track can be read in between writing clock bursts and the read signal is used to frequency-lock an oscillator which is used as a reference for the writing of clock bursts of the next track. A checkerboard pattern of clock bursts is thus created. All subsequent tracks are built incrementally by stepping off a half of a track from the last track written, which comprises clock bursts, and writing a next new sequence of clock bursts that interlace with the previous track's clock bursts.

An advantage of the present invention is that it provides a disk drive that maintains with its own resources accurate radial and rotational positional references.

An advantage of the present invention is that it provides a disk drive that does not require an external positioner/actuator or feedback system.

Another advantage of the present invention is that it provides a disk drive that can write servo bursts that are wider and/or narrower than the flux gap of the transducer.

An advantage of the present invention is that it provides a disk drive that is economical to manufacture because expensive servowriting equipment is not required.

A still further advantage of the present invention is that it provides a disk drive that can do field rewrites of corrupted or damaged servo information.

Another advantage of the present invention is that it provides a disk drive that maintains synchronous transition placement between tracks.

Another advantage of the present invention is that it provides a disk drive that is compatible with zone bit recording techniques.

These and other objects and advantages of the present invention will no doubt become obvious to those

of ordinary skill in the art after having read the following detailed description of the preferred embodiments which are illustrated in the various drawing figures.

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IN THE DRAWINGS

Fig. 1 is a functional block diagram of a hard disk drive embodiment of the present invention;

Fig. 2A is a top view of a portion of a clock track before and after a reference gap in which the clock track has been recorded in a concentric ring on a surface of a disk;

15 Fig. 2B is a top view of the clock track of Fig. 2A after a first track sequence of A-bursts have been recorded a half of a track width offset from the clock track;

20 Fig. 2C is a top view of the tracks of Fig. 2B after a first track sequence of clock-bursts have been recorded a half of a track width offset from the A-burst track;

Fig. 2D is a top view of the tracks of Fig. 2C after a sequence of B-bursts have been recorded a half of a track width offset from the first clock-burst track;

25 Fig. 2E is a top view of the tracks of Fig. 2D after a second sequence of clock bursts have been recorded a half of a track width offset from the B-burst track;

30 Fig. 2F is a top view of the tracks of Fig. 2E after a second track sequence of A-bursts have been recorded a half of a track width offset from the second sequence of clock bursts;

Fig. 2G is a top view of the first and last sectors of four tracks after writing user data areas that leave a single set of AB-bursts per sector;

35 Fig. 3 is a block diagram of a system embodiment of the present invention for using the methods of the present invention in conjunction with commercially

available head disk assemblies; and

Fig. 4 is a flowchart of a method of the present invention executable by the system of Fig. 3 or the disk drive of Fig. 1 to produce the track patterns of Figs. 2A through 2G.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

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Fig. 1 illustrates a hard disk drive 10 comprising a rotating disk 12 with a recording surface 14, a spindle motor 15, a transducer 16 in communication with the surface 14 and swept radially over the surface 14 by a servo-actuator 18, a variable gain read amplifier 20 connected to the transducer 16, an analog-to-digital converter (ADC) 22 attached to the variable gain read amplifier 20, an erase frequency oscillator 24 coupled to the transducer 16 for direct current (DC) erasing of the disk surface 14, a memory 26 for storing a plurality of digital output signals 28 appearing at the ADC 22, a variable clock oscillator (VCO) 29 that provides an output reference frequency and can be locked-on to a sample clock read by transducer 16, and a controller 30 for signaling the servo-actuator 18 to move to such radial positions that result in transducer read amplitudes that are a predefined percentage of previous read amplitudes represented in the digital memory 26. Actuator 18 may comprise a conventional voice coil motor.

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In actual operation, it may not be reasonable to assume that surface 14 is clean of signals and erased, therefore the surface 14 must be erased track-by-track without the aid of servotracks. Thereafter, servotracks are later written by a method described in connection with Fig. 4. Preferably, an integrated circuit 32 is included in disk drive 10 to control the erasing and writing of servotracks and associated patterns on surface

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14. Spindle motor 15 is controlled by IC 32 to rotate surface 14 at a nominal drive velocity. IC 32 causes transducer 16 to write either a constant frequency signal or one that is slewed between a fundamental frequency (1F) and twice the fundamental frequency (2F).
5 Simultaneously, servo-actuator 18 is caused to sweep transducer 16 between a pair of inner and outer mechanical limits (34 and 36). The sweeping and writing is continued by IC 32 for at least fifty passes and is
10 intended to insure that some non-coherent data will be in every useful track location. Transducer 16 is then positioned at inner mechanical limit 34 and directed to write the 2F signal for at least one disk revolution period. The transducer is caused to read and the
15 variable gain read amplifier is adjusted such that the maximum amplitude signal read from the disk is between ninety and one hundred percent (90%-100%) of the ADC, which functions as an amplifier output measurement device. A digital value produced by the ADC is a
20 representation of a complete track amplitude maximum and is stored in memory for later reference by IC 32. The present track is then DC-erased by causing transducer 16 to be energized in one polarity for a time longer than one complete disk revolution period. IC 32 signals to
25 the servo-actuator, cause transducer 16 to move away from the inner mechanical limit 34 while transducer 34 is reading. As soon as any data read is half of the stored full track amplitude reference, transducer movement is stopped. If the read amplitude of any data is greater
30 than fifty percent, the transducer is moved toward the inner mechanical limit 34 until the maximum amplitude is fifty percent of the full track amplitude. At such a point, the transducer is probably positioned half over random data and half over the previously erased part of
35 surface 14. As before, signals from IC 32 cause a DC-erase signal to be sent to transducer 16 for at least one disk revolution time period. The steps of moving and

erasing are repeated until the transducer encounters the outer mechanical limit 36 as sensed by the inability of the servo-actuator to move transducer 16 to a fifty percent amplitude point. Substantially all of a useful portion of surface 14 is thereby erased in preparation for servo-patterning.

Figs. 2A through 2G illustrate, with a sequence of increment steps, how the surface 14 is written with servotracks and prepared ultimately for user data storage.

IC 32 causes a clock track 50 "with closure" to be written on surface 14 (Fig. 1) along a centerline 52 that is proximate to outer limit 36 (Fig. 1). Closure is defined as the condition in which one complete revolution of a track comprises a known number of equidistant clock transitions with a last transition having substantially the same spacing as a first transition, or at least within ten percent of being equal. Reading the clock track 50 will produce a constant frequency signal with a known number of clock transitions per revolution. In order to mark a beginning of each revolution of the disk, a gap 54 is created in the clock track 50 which comprises an integer number of missing clock transitions, e.g., three transitions (3T) to thirty transitions (30T). A count of the total number of clock transitions in a clock track 50, as treated herein, includes the omitted clock transitions in the reference gap 54.

In order for IC 32 to propagate servotracks and their associated half-track offset "A" and "B" bursts without the aid of an external servowriter and laser position determination instrument, clock track 50 is divided into a number of sectors approximately equal to that number of user data sectors anticipated for drive 10 in its final application. Thirty-two sectors is a typical value. Each sector is divided into an even integer number of segments. Each segment has to be long enough to allow transducer 16 to recover from a

transducer write/read channel write to read transition and long enough for the VCO 29 to lock-on. However, each segment must not be so long that VCO 29 can drift without active input correction for more than, preferably, 0.01 percent. Nor should each segment be so long that the disk rotation time can drift by more than 0.01 percent. The VCO drift time and disk rotational drift time will, of course, be dependent on particulars of individual disk drive implementations, and may be determined empirically. Ten to fourteen segments per sector are typical numbers. For example, with thirty-two sectors and fourteen segments per sector, there will be 448 segments per whole clock track 50. For drawing clarity, Figs. 2A through 2G show only the first few and the last few segments of a track and are not necessarily illustrated to scale, e.g., ten segments are the same length as one sector.

The speed of the motor 15 may be adjusted by IC 32 such that the relationship between the rotational period and transducer write frequency results in the number of transitions per revolution being an exact integer number of transitions per segment and a more-or-less equal number of transitions per segment. Gap 54 is created by controlling transducer 16 to write DC for a period corresponding to 30T and then a 2F frequency is written for a number of transitions minus three which will exactly fill one revolution of a track. The number of transitions per revolution is preferably calculated using the disk rotational period and the drive 2F frequency. Transducer 16 measures the results for proper gap length by reading back the just recorded track 50 and non-data gap 54. If gap 54 is shorter than 3T, spindle motor 15 is controlled by IC 32 to slow the disk rotational speed, e.g., approximately 0.1 percent. If gap 54 is longer than 30T the spindle motor speed is increased. The procedure of DC-erasing followed by writing 2F data is repeated and the length of gap 54 measured again. When the length of gap 54 is between 3T and 30T, it is then further checked

to determine if there are an integer number of transitions. If a division remainder (gap 54 length divided by the T period length) is less than ten percent of a T-period length, the clock track 50 is closed. The
5 ten percent criterion is arbitrary, and other values may be used according to user requirements.

An exact total number of transitions in clock track 50 is determined by calculating the number of transitions that could be placed in gap 54 and adding the number of
10 transitions read in the remainder of the revolution or by counting the number of transitions as they are written, and adding the number that could fit in gap 54. Since it is unlikely that the total number of transitions in the revolution will be an integer multiple of 448, 447
15 segments will contain an equal number of transitions and a 448th segment will contain the same number of transitions plus any remaining number of transitions.

In order to determine when transducer 16 has been moved one-half track width inward from centerline 52, a
20 reduction in data read amplitudes is used as an indicator. A table of on-track data amplitudes is gathered and stored in memory 26. To determine position relative to track 50, transducer 16 is caused to read continuously while on centerline 52 and the gain of
25 amplifier 20 is adjusted to have its maximum output in any one revolution between ninety and one hundred percent of the full-scale measurement range of ADC 22. The gain of amplifier 20 is then fixed, and an amplitude sample is taken in each half sector for every sector in the
30 revolution and their digital values stored in the table in memory 26. Sixty-four samples in all are typically gathered. Each amplitude sample value is divided by two before being stored in the table. Differences among the amplitudes of individual samples are often the result of
35 variations in disk magnetic coating thickness and characteristics. Too wide a difference indicates a defective disk surface 14. A difference between the

minimum and maximum value is computed by IC 32 and compared against a user-selected value. If exceeded, the disk 12 is rejected as having excessive modulation and the servowriting process is preferably terminated.

5 In typical disk drives, flying height variations of a transducer will occur due to the media not being absolutely flat and/or the media surfaces not being at perfect right angles to the axis of the spindle. In the present example, as transducer 16 is forced away from
10 surface 14, a side-fringing of the write signal will cause the track being written to be wider than a flux gap within transducer 16. Variations in read amplitude will also follow flying-height variations, even if the track being read has a perfectly uniform width. If transducer
15 16 experiences variations in flying height, the edges of track thus read or written will wobble in and out with respect to a perfect circle concentric on the axis of the spindle.

 Servo-actuator 18 is controlled by IC 32 to keep
20 transducer 16 at a radial position that maintains a predetermined read amplitude. Fig. 2B shows this position as centerline 56, which is a half-track width from centerline 52. A series of target servo amplitudes is input from the table in memory 26 corresponding to the
25 approximate longitudinal position of transducer 16 along track 50. As transducer 16 reads each first half of a sector, a half-amplitude for that sector's half from memory 26 is used for a servo target amplitude. As disk 12 rotates, the servo target amplitude is updated with
30 the next value. IC 32 compares the actual read amplitudes with the servo target amplitudes and controls servo-actuator 18 so that the two amplitudes converge and reconverge in realtime as track 50 moves through its whole length under transducer 16.

35 Further refinement of the half-amplitude servo position scheme is possible. Hunting can be reduced or substantially eliminated by a process of smoothing the

servo error control signals. For each of sixty-four servo sample points, a read amplitude is compared with the target amplitude from the table in memory 26. A mean, or average value between corresponding target and measured amplitudes may be stored in the table as a series of new target values. Such smoothing is preferably repeated for three revolutions of disk 12. If the difference between any target and measured value is greater than a user selected percentage, disk 12 may be rejected for having excessive modulation and the process terminated. After three revolutions of smoothing, transducer 16 may be servo controlled by IC 32 half-offset from an adjacent track without significant hunting or dither, and will be positioned exactly.

Transducer 16 is positioned to write the next track when it is over centerline 56 (Fig. 2B). Every possible segment location is not written on an A-burst track 58. Instead, every other segment is written such that parts of track 50 still reach centerline 56, and those parts are available to keep the write oscillator 29 synchronized with clock track 50 while writing track 58. Therefore, one segment per half sector may be used to servo transducer 16 and provide enough points of correction to maintain a correct radial position on centerline 56. Such alternate segments may be written with data generated from a memory image of a sector construction. Three pattern types are ultimately used: an A-servo burst such as in track 58, a B-servo burst and a drive sector ID field including a Grey code or some other track identification code. Only a first segment in each sector is in position to ultimately become the A-burst of a user AB-type servo burst. In order not to lose the rotational position of the once-around-marker, gap 54, the first segment of each track of any servo burst is preferably written to include a once-around-marker, for example, a 12T DC-erased gap.

Once 224 full track-width segment bursts comprising

track 58 are written, transducer 16 is caused to read and amplifier 20 gain is controlled by IC 32 to result in a maximum reading between ninety and one hundred percent while over centerline 56. Again, one amplitude sample
5 per sector, this time of track 58, is gathered, then divided by two and stored in the table in memory 26 as a new set of servo target amplitudes. Servo-actuator 18 is controlled by IC 32 while using table target values as amplitude references for a correct transducer placement
10 off the most recently written segment bursts, track 58. This positions transducer 16 one-half a track width closer to the inner radius from the most recently written segment bursts, along a centerline 60 (Fig. 2C). Again, for three revolutions, the target amplitudes are compared
15 to a series of read-in amplitudes, and the mean values are computed and stored by IC 32 in memory 26 as the new target servo amplitude values. Thus track edge smoothing is repeated. After which such process transducer 16 can be assumed to be positioned exactly one-half a track
20 width toward the inner radius from the previously written segments. A series of alternating segments are written in a track 62 while switching back transducer 16 every other segment time to read-in parts of track 58 in order to keep VCO 29 locked-in and synchronized to the clock
25 transitions within track 58 and thus to also be synchronized with the clock transitions constituting clock track 50. As before, the back and forth switching between reading a previous track to lock-in VCO 29 and writing bursts for the next track 62 maintains bit-
30 synchronism track-to-track. The segments which are selected for writing track 58 are one segment shifted from those segments selected for writing track 62. This allows a checkerboard pattern of sample clock transitions that may be read at half amplitude while writing a next
35 track. One segment in each sector will ultimately be a user sector header field and will contain sync fields, Grey codes or other track identification. These user

definable data may exist as model patterns within a micro processor memory and may be written at this point.

The process of reading full track amplitude, storing half the amplitude, using these half-amplitudes to servo to the next half track, edge-smoothing and writing the next 224 segments is repeated again along a centerline to create a B-burst track 66 (Fig. 2D). Although several segments per sector are typically written, only one segment per sector, the first segment, will ultimately become the B-burst of a user AB-servo burst.

Fig. 2E shows a centerline 68 a half a track width offset from centerline 64. A track 70 is recorded along centerline 68 with bursts in segments interlaced with those of track 66.

Fig. 2F shows a centerline 72 a half a track width offset from centerline 68. A track 74 is recorded along centerline 72 with bursts in segments interlaced with those of track 70.

This incremental process is repeated until transducer 16 encounters a mechanical stop, e.g., limit 34. A limit is recognized by seeing read amplitudes that cannot be reduced to respective half-amplitudes by continuing to increase the voice coil current to servo-actuator 18. Preferably a count of the tracks written has been maintained by IC 32 in memory 26, and if the total number of tracks written between the outer to inner mechanical stops is not within a user preset range, the process is restarted at the DC-erase step from the inner stop step and the fifty percent of full track amplitude servo target is changed to a value inversely related to the percentage error in the track count. This process is continuously re-corrected and re-started until a track count within the user selected range is obtained.

Fig. 2G shows the beginning and ending sectors of the first four tracks thus obtained. A user data area 76 within each sector is overwritten such that an AB-burst pair straddles the centerline of each user data area 76.

When transducer 16 is not exactly centered over the user data area 76, one of the A-burst or B-burst will be read with an amplitude greater than the other, and servo-actuator can be controlled to correct the positioning error.

If servo tracks are to be written to more than one surface on a disk drive, after transducer 16 has written the new segments servoing and using the half-amplitude segments for VCO synchronization, transducers 16 associated with other disk drive surfaces 14 write and alternately read the first surface for a clock reference. This causes the data written to the other surfaces to be exactly placed rotationally and radially with respect to the first surface.

Conventional disk drives without special provisions or interfaces for self-servowriting can nevertheless be servowritten with the aid of a system 80 illustrated in Fig. 3. The system 80 comprises a personality module 82 that interfaces to a transducer 84, a positioner 86 and a spindle motor 88 within an external and conventional head disk assembly (HDA) 90. HDA 90 is a commercially available unit as marketed by Conner Peripherals, Seagate, NEC and others. System 80 further comprises a microprocessor 92, a memory 94 including both read-only memory (ROM) and random access memory (RAM), a sequencer 96, a VCO 98, a pattern detector 100, an ADC 102 and a user interface 104. Personality module 82 is detachable and may be replaced with another module suited to HDA 90. Several specific personality modules 82 may be constructed to interface system 80 to a range of commercially available HDAs 90. The interfacing requirements, will of course, depend on the particular characteristics of the HDA 90. Cabling between personality module 82 and HDA 90 preferably conforms to connector formats included in HDA 90.

Personality module 82 comprises a read/write (R/W) channel 106, a servo controller 108 and a spindle speed

controller 110. Zero-crossing and level signals are communicated from R/W channel 106 to pattern detector 100, which, in turn, communicates those and match and data signals to sequencer 96. Non-return-to-zero (NRZ),
5 write gate and sample/hold signals are output from sequencer 96 to R/W channel 106. VCO 98 provides a clock signal to both sequencer 96 and pattern detector 100. The configuration and programming mechanics of microprocessor 92 and memory 94 are conventional. The
10 uses of memory 26 (Fig. 1) may also be supported by memory 94, as well as the activities of IC 32 supported by microprocessor 92 executing the procedures described in connection with Fig. 4. Transducer 84 is similar to transducer 16 (Fig. 1), as are positioner 86 to servo-
15 actuator 18 (Fig. 1) and motor 88 to motor 15 (Fig. 1). It would be practical to incorporate the elements of system 80 within an otherwise conventional HDA 90 to gain the self-servowriting benefits of the present invention in a standalone device.

20 A method embodiment of the present invention is illustrated with the flowchart of Fig. 4. A process 120 comprises a step 122 of moving transducer 16 (Fig. 1) to a first mechanical stop limit with servo-actuator 18. A step 124 includes writing a first frequency signal to
25 transducer 16 for a period longer than the time period of a single revolution of the disk 14. A step 126 includes reading said first frequency signal with variable gain amplifier 20. A step 128 adjusts the gain of amplifier 20 to result in a digital value 28 at the output of ADC
30 22 that is within the range of ninety to one hundred percent of a full-scale range of said ADC and storing said digital value in memory 26. A step 130 includes erasing a first track on the disk surface with direct current applied to transducer 16. A step 132 includes
35 controlling the actuator 18 to move transducer 16 away from the first stop limit toward a second stop limit while simultaneously reading a signal from transducer 16.

A step 134 includes stopping the movement of the actuator 18 when the amplitude of a signal read by transducer 16 is approximately one-half of a value represented by the ADC digital value read from memory 26. A step 136 includes repeating the steps of erasing, controlling and stopping for successive tracks until the second stop limit is encountered by servo-actuator 18, wherein the useful area of disk surface 14 is substantially erased. A step 138 writes clock track 50 with transducer 16 to result in a sequence of signal transitions in which a last transition joins with a first transition to produce a whole integer number of transitions. A step 140 includes omitting a fixed number of clock transitions in clock track 50 to create reference gap 54. A step 142 checks the reference gap length. A step 144 includes partitioning clock track 50 into a plurality of sectors and each of the sectors into a plurality of segments, wherein a number of clock transitions are contained within each segment. A step 146 causes a DC-erasing of a number of clock transitions in each of a first and a second segment of each sector to divide all the sectors into two parts and in relation to the reference gap in the clock track 50. A step 148 builds a table of track amplitude measurements in memory 26 by sampling, via transducer 16 and variable gain amplifier 20, a signal obtained from segments in each of the two parts of each sector and digitized by ADC 22 and storing half of each corresponding digital value in memory 26. A step 150 compares a lowest digital value in memory 26 table thus obtained with a highest digital value in memory 26 and rejects the disk drive as defective if the comparison exceeds a predetermined amount. A step 152 includes positioning transducer 16 one-half track toward the second stop limit with servo-actuator 18, as determined by comparing segment-by-segment and sector-by-sector a plurality of read amplitudes obtained simultaneously from transducer 16 with their corresponding digital values

used as target values in memory 26. A step 154 includes writing a first pattern of interleaving segments and reading intervening segments from a preceding clock track 50 with transducer 16 to maintain a frequency lock of clock oscillator VCO 29 which is used to generate the clock transitions wherein track-to-track bit-synchronism is maintained. A step 156 includes positioning transducer 16 one-half track further toward the second stop limit with servo-actuator 18 as determined by comparing a plurality of read amplitudes related to the first pattern obtained simultaneously from transducer 16 with their corresponding digital values used as target values in memory 26. A step 158 includes writing a second pattern of segments aligned side-by-side with segments of the first pattern and reading intervening segments from a preceding clock track 50 with transducer 16 to maintain a frequency lock of a clock oscillator used to generate the clock transitions wherein track-to-track bit-synchronism is maintained. Figs. 2A through 2G are representative of the condition of surface 14 as process 120 progresses from step to step.

Although the present invention has been described in terms of the presently preferred embodiments, it is to be understood that the disclosure is not to be interpreted as limiting. Various further alterations and modifications will no doubt become apparent to those skilled in the art after having read the above disclosure. Accordingly, it is intended that the appended claims be interpreted as covering all alterations and modifications as fall within the true spirit and scope of the invention.

What is claimed is:

1. A self-servowriting hard disk drive, comprising:
 - a flat circular disk including a surface coated with a recording medium and means for rotating the disk;
 - 5 a recording transducer in communication with said recording medium surface;
 - servo-actuator means for positioning the recording transducer at a plurality of radial positions relative to said recording medium surface; and
 - 10 positioning means for recording a first servotrack on said recording medium surface, for reading via the recording transducer a plurality of samples of said first servotrack, for storing said plurality of samples in a memory table, for signaling the servo-
 - 15 actuator means to move radially in an initial first direction until a signal read by the recording transducer is approximately half of the amplitude of a corresponding sample stored in said memory table, for signaling the servo-actuator means to follow a path defined a plurality
 - 20 of subsequent signals read by the recording transducer to be approximately half of the amplitude of a plurality of corresponding samples stored in said memory table, and for recording a second servotrack on said recording medium while the servo-actuator means is following said
 - 25 defined path.

2. The drive of claim 1, further including:
 - clock generation means for a first writing of a first group of an integral number of whole clock
 - 30 transitions along said first servotrack and then frequency locking to said clock transitions thus recorded on said first servotrack and read back to thereafter control a second writing of a second group of clock
 - transitions on said second servotrack that are bit-
 - 35 synchronous with said clock transitions on said first servotrack.

3. A self-servowriting disk drive, comprising:
a flat circular disk for rotation by a spindle
motor means;

a recording medium surface disposed on a
5 surface of the disk;

a recording transducer in communication with
the recording medium surface;

voice-coil servo-actuator means for
positioning the recording transducer at a plurality of
10 radial positions relative to the recording medium
surface; and

integrated circuit controller means for
recording a first servotrack on the recording medium
surface via said recording transducer, for reading a
15 plurality of analog samples of said first servotrack with
said recording transducer, for converting said plurality
of analog samples to a plurality of digital samples, for
storing said plurality of digital samples in a memory
table, for signaling the servo-actuator means to move
20 radially in an initial first direction until an analog
signal read by the recording transducer is approximately
half of the amplitude represented by a corresponding
digital sample stored in said memory table, and for
signaling the servo-actuator means to follow a path
25 defined a plurality of subsequent analog signals read by
the recording transducer to be approximately half of the
amplitude represented by a plurality of corresponding
digital samples stored in said memory table.

30 4. The disk drive of claim 3, wherein:

the recording medium surface is a dedicated
servo disk surface and data are recorded to other
surfaces of the disk.

35 5. The disk drive of claim 3, wherein:

the recording medium surface comprises
material for magnetic recording; and

the recording transducer is a magnetic read/write head.

6. The disk drive of claim 3, wherein:

5 the recording medium surface comprises material for optical recording; and
 the recording transducer is an optical read/write head.

10 7. The disk drive of claim 3, wherein:

 the disk is a flexible diskette that may be removed from the drive.

8. An integrated circuit (IC) for inclusion within
15 an electronics assembly associated with a hard disk drive and for controlling a servo-actuator and transducer within the disk drive between a first and a second mechanical stop such that the disk drive may self-write a plurality of servotracks on a surface of a disk included
20 within the disk drive, the IC comprising:

 direct current (DC) erasing means for applying an erasing signal to the transducer;

 variable gain read amplifier means for receiving a read signal from the transducer and for
25 amplifying said read signal by a programmable multiplier;

 analog-to-digital converter (ADC) means for converting an analog amplified read signal from the variable gain read amplifier means to a digital equivalent values;

30 table memory means for storing said digital equivalent values;

 servo control means for controlling bi-directional movements of the servo-actuator;

 first clock track writing means for causing
35 the servo-actuator to move to the second mechanical stop and for writing a clock track with closure wherein a whole integer number of clock transitions are written by the transducer on the disk surface with a gap having a whole integer number of missing clock transitions to

serve as a reference;

first servo-pattern writing means for moving the servo-actuator toward the second mechanical stop one-half track and for writing first alternate bursts and
5 reading a next outward track between alternate bursts to frequency lock the writing;

second clock pattern writing means for moving the servo-actuator toward the second mechanical stop one-half track and for writing clock bursts between said
10 first alternate bursts and reading said first alternate bursts between said first clock bursts to frequency lock the writing;

second servo-pattern writing means for moving the servo-actuator toward the second mechanical stop one-half track and for writing second alternate bursts
15 radially aligned to said first alternate bursts and reading a next outward track between said second alternate bursts to frequency lock the writing wherein said first and second alternate bursts aid in positioning
20 the servo-actuator;

third clock pattern writing means for moving the servo-actuator toward the first mechanical stop one-half track and for writing clock bursts between said
first alternate bursts and reading said first alternate
25 bursts between said first clock bursts to frequency lock the writing;

frequency synchronization means for generating and maintaining a reference frequency to be used by the first through third clock pattern writing means and first
30 and second servo-pattern writing means and for locking to a clock burst obtained periodically by the transducer between writing said first and second alternate bursts wherein track-to-track bit-synchronization may be maintained; and

35 servowriting repetition means for activating the first and second servo-pattern writing means and second and third clock pattern writing means in a

sequence that will fill the surface of the disk with a predetermined number of servo-patterns arranged in a plurality of concentric rings.

5 9. The IC of claim 8, further comprising:

 half-track position determination means for
sampling a plurality of read amplitudes obtained by the
transducer and scaled by the variable gain read amplifier
means and converted by the ADC means to a plurality of
10 corresponding digital values that are stored in the
memory table means and for then comparing said stored
digital values to a plurality of samples obtained from
the transducer while simultaneously causing the servo-
actuator to move the transducer inward or outward
15 according to said comparison.

 10. The IC of claim 8, further comprising:

 servo-smoothing means for collecting and
averaging said sampled plurality of read amplitudes over
20 three revolutions of the disk with said plurality of
digital values stored in the memory table means wherein
the servo-actuator is controlled without substantial
hunting for a half-track position indicated by half-
amplitude read signals.

25

 11. A method for writing servotracks on a recording
surface of a disk drive that includes a rotating disk, a
transducer, and an actuator for moving said transducer
radially over said surface of said disk, comprising the
30 steps of:

 moving said transducer to a first stop limit
with said actuator;

 writing a first frequency signal on said disk
with said transducer for a period of time longer than
35 that necessary for a single revolution of said disk;

 reading said first frequency signal with a
variable gain amplifier connected to said transducer;

adjusting a gain of said variable gain amplifier to provide a digital value at the output of an analog-to-digital converter (ADC) that is within the range of ninety to one hundred percent of a full-scale
5 range of said ADC and storing said digital value in a memory;

erasing a first track on said disk surface with current applied to said transducer;

controlling said actuator to move said
10 transducer away from said first stop limit toward a second stop limit while simultaneously reading a signal from said transducer;

stopping said movement of said actuator when the amplitude of a signal read by said transducer is
15 approximately one-half of a value represented by said ADC digital value read from said memory;

repeating the steps of erasing, controlling and stopping for successive tracks until said second stop limit is encountered by said actuator wherein said disk
20 surface is substantially erased;

writing a clock track with said transducer to result in a sequence of signal transitions in which a last transition joins with a first transition to produce a whole integer number of transitions in said clock
25 track;

omitting a fixed number of clock transitions in said clock track to create a reference gap;

dividing said clock track into a plurality of sectors and each sector into a plurality of segments
30 wherein a number of clock transitions are contained within each segment;

sampling (with said transducer and variable gain amplifier) a signal obtained from each of said two parts of each sector and digitized by said ADC and
35 storing half of each corresponding digital value in a memory table built from such track amplitude measurements;

comparing a lowest digital value in said table with a highest digital value in said table and rejecting said disk drive as defective if the comparison exceeds a predetermined amount;

5 positioning said transducer one-half track toward said first stop limit with said actuator as determined by comparing segment-by-segment and sector-by-sector a plurality of read amplitudes obtained simultaneously from said transducer with their
10 corresponding digital values used as target values in said memory table;

 writing a first pattern of interleaving segments and reading intervening segments from a preceding clock track with said transducer to maintain a
15 frequency lock of a clock oscillator used to generate said clock transitions wherein track-to-track bit-synchronism is maintained;

 positioning said transducer one-half track further toward said first stop limit with said actuator
20 as determined by comparing a plurality of read amplitudes related to said first pattern obtained simultaneously from said transducer with their corresponding digital values used as target values in said table; and

 writing a second pattern of segments aligned
25 side-by-side with segments of said first pattern and reading intervening segments from a preceding clock track with said transducer to maintain a frequency lock of a clock oscillator used to generate said clock transitions wherein track-to-track bit-synchronism is maintained.

30

12. The method of claim 11, further including between the steps of positioning said transducer and writing a pattern, the step of:

 averaging over a plurality of revolutions of
35 said disk said read amplitudes with said target values wherein actuator hunting is substantially reduced and radial variations in recording amplitude are determined.

13. The method of claim 11, wherein:

said table is built by sampling with said transducer and variable gain amplifier:

5

14. The method of claim 11, wherein:

said disk surface is erased with direct current applied to said transducer.

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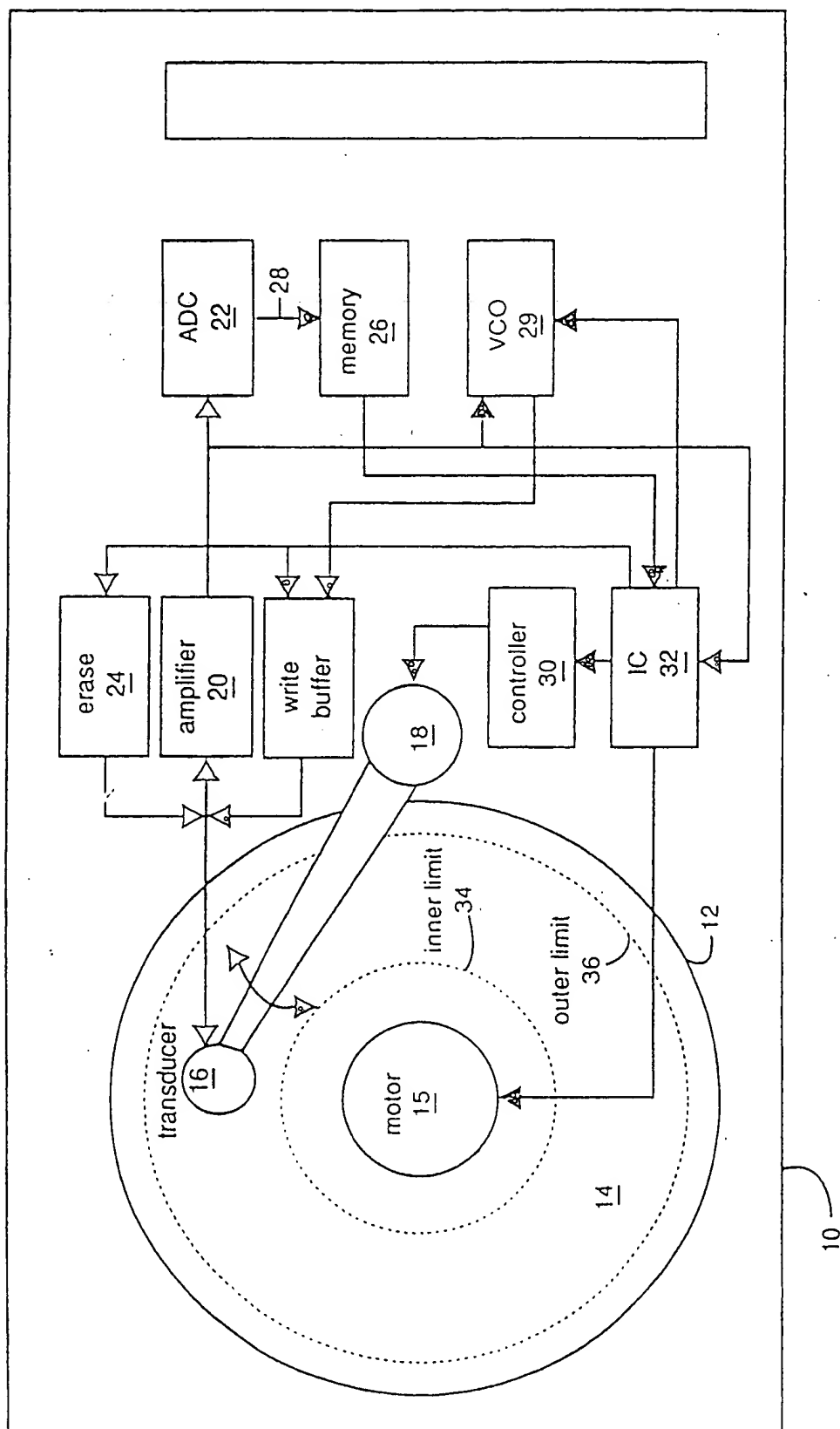
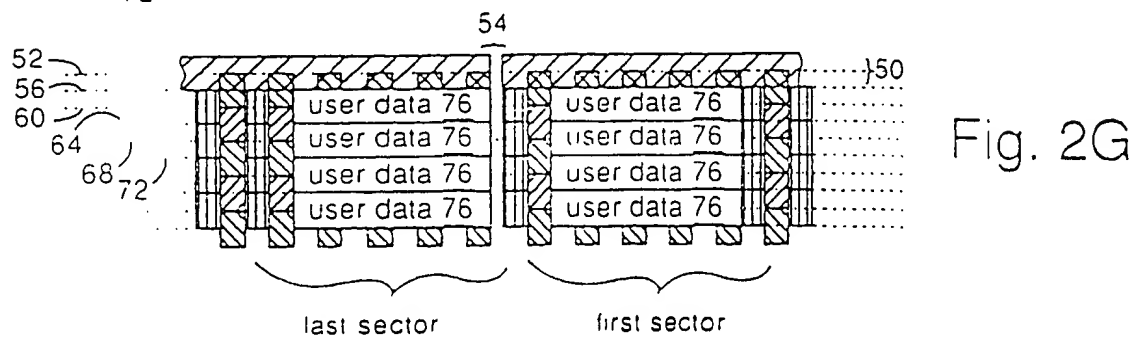
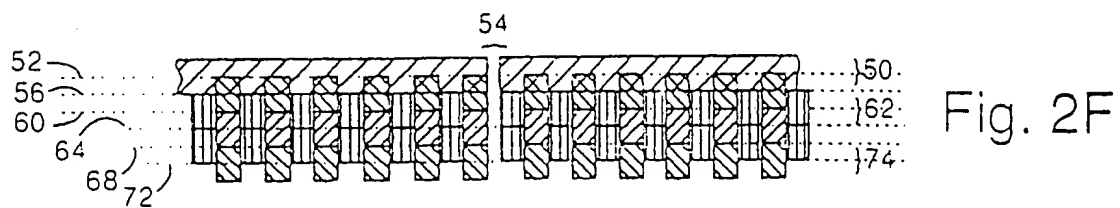
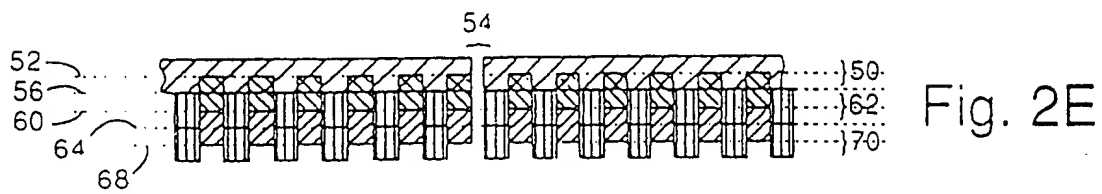
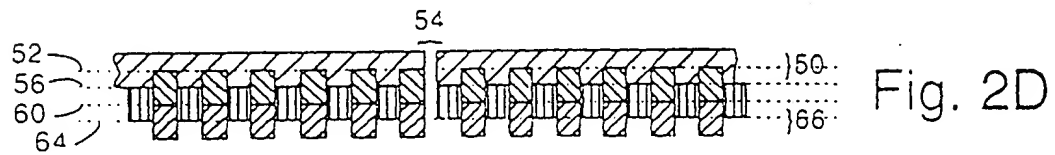
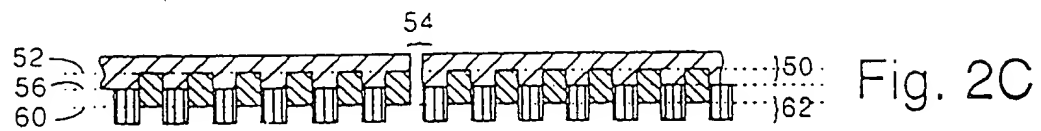
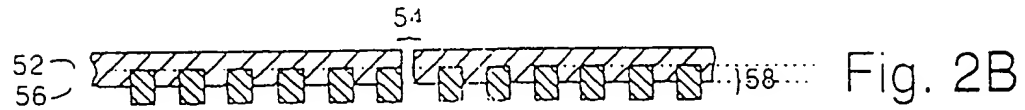
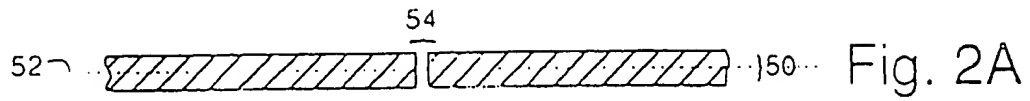
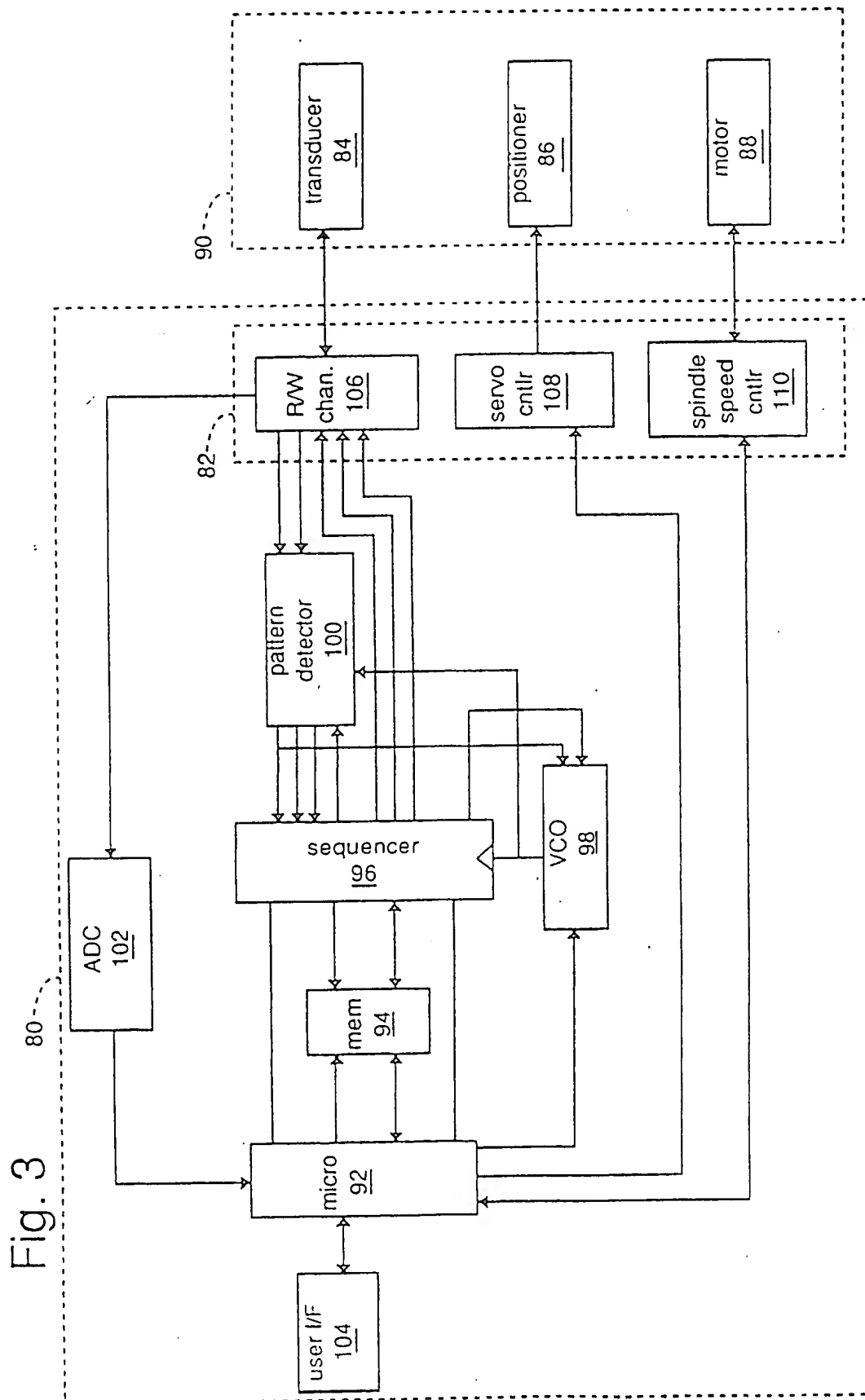


Fig. 1

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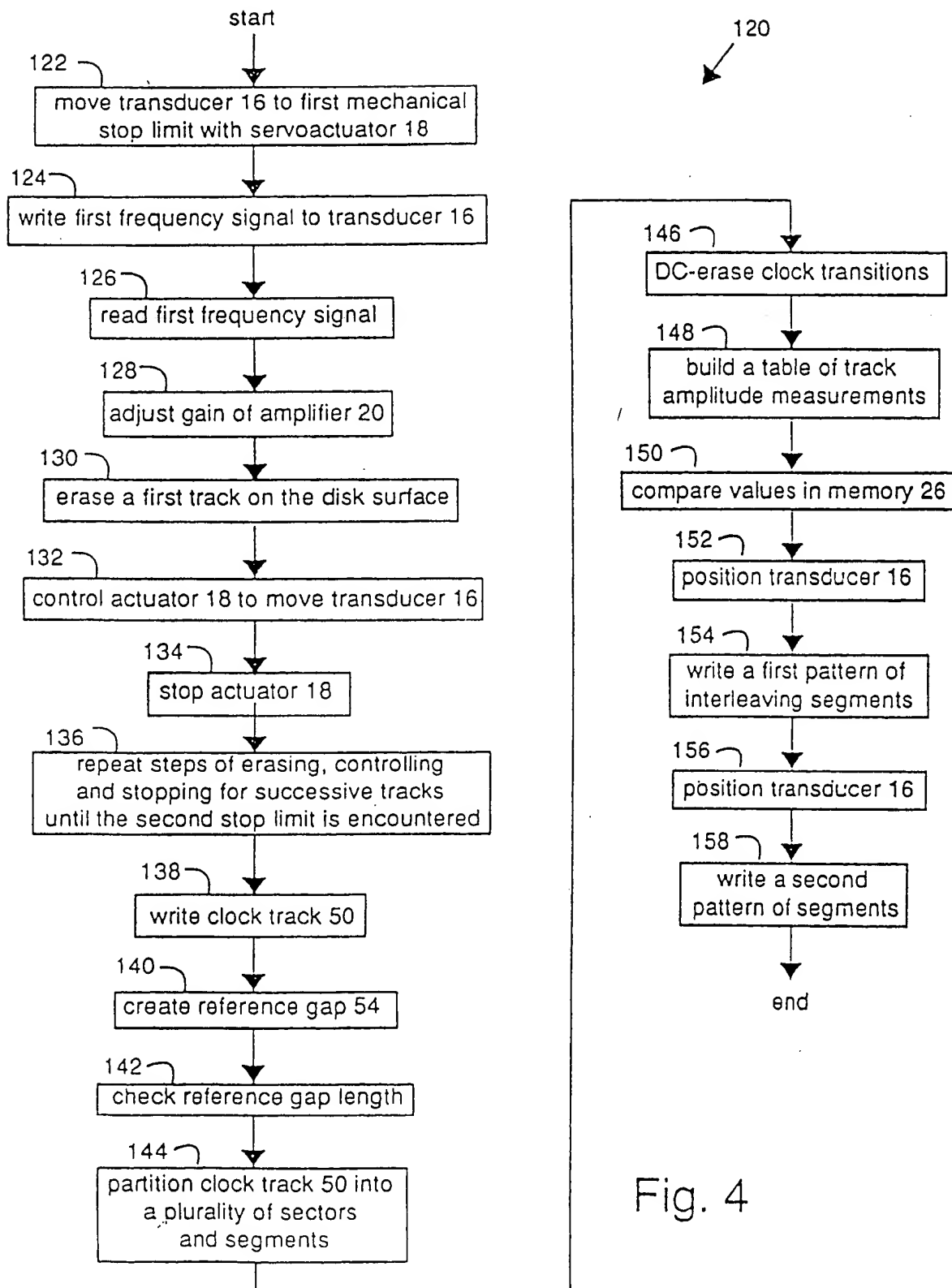


Fig. 4

A. CLASSIFICATION OF SUBJECT MATTER

IPC(5) : G11B 5/596, 3/90

US CL : 360/ 77.06, 78.04; 369/58

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 360/ 27, 31, 48, 51, 57, 66, 75, 77.02, 77.06, 78.04; 369/58

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

APS and JPOABS

terms: adjust, gain, disk, write

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X --- Y	US, A, 4,414,589 (OLIVER ET AL) 08 NOV 1983, Figs 4, 8-10	1,2 ---- 3-14
X -- Y	US, A, 4,912,576 (JANZ) 27 MARCH 1990, col.9, lines 15-25	1 ---- 2-14
Y,P	US, A, 5,256,965 (NOMURA) 26 OCT 1993, Fig. 4	3-14
Y,P	US, A, 5,175,719 (IIMURA) 29 DEC 1992, Fig. 1, Abstract	11-14
Y	US, A, 4,734,900 (DAVIE) 29 MARCH 1988, Abstract	8-10

☒ Further documents are listed in the continuation of Box C.
 ☐ See patent family annex.

* Special categories of cited documents:	"T"	later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"A" document defining the general state of the art which is not considered to be part of particular relevance	"X"	document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"E" earlier document published on or after the international filing date	"Y"	document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"&"	document member of the same patent family
"O" document referring to an oral disclosure, use, exhibition or other means		
"P" document published prior to the international filing date but later than the priority date claimed		

Date of the actual completion of the international search

26 January 1994

Date of mailing of the international search report

07 FEB 1994

 Name and mailing address of the ISA/US
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 Box PCT
 Washington, D.C. 20231

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Authorized officer

/s/ JIM WILSON

Telephone No. (703) 308-0956

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y,P	US, A, 5,193,034 (TSUYOSHI ET AL) 09 MARCH 1993, Abstract	8-10

Box I Observations where certain claims were found unsearchable (Continuation of item 1 of first sheet)

This international report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. ☐ Claims Nos.:
because they relate to subject matter not required to be searched by this Authority, namely:
2. ☐ Claims Nos.:
because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically:
3. ☐ Claims Nos.:
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

Box II Observations where unity of invention is lacking (Continuation of item 2 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:

- I. Claims 1-7, drawn to controlling an actuator based on a read signal, classified in Class 360, subclass 78.04.
- II. Claims 8-10, drawn to writing a servo track based on read clock transitions, classified in Class 360, subclass 77.06.
- III. Claims 11-14, drawn to a method of erasing a disk and checking for defects, classified in Class 369, subclass 58.

The inventions of groups I and II lack the inventive concept of checkin

The inventions of groups II and III lack the inventive concept of positio

The inventions of groups I and III lack the synchronization to a clock p

1. ☒ As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims.
2. ☐ As all searchable claims could be searched without effort justifying an additional fee, this Authority did not invite payment of any additional fee.
3. ☐ As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims for which fees were paid, specifically claims Nos.:
4. ☐ No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:

Remark on Protest

☐

The additional search fees were accompanied by the applicant's protest.

☒

No protest accompanied the payment of additional search fees.